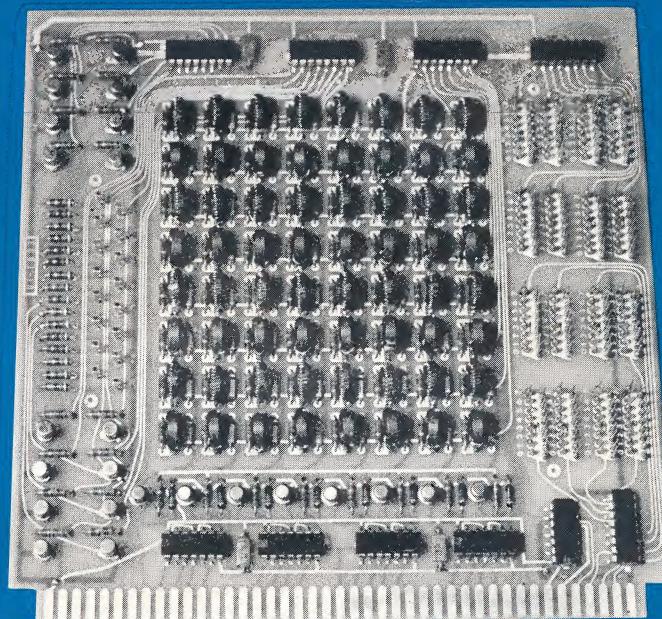


# LOW COST READ ONLY MEMORY

## UI-1003

**RUGGED CONSTRUCTION - HIGHEST RELIABILITY**

- LOWEST COST
- 350 nS CYCLE TIME
- 5 VOLT POWER SUPPLY
- SIZE: 7-5/8" x 7-1/2" x 3/4"
- COMPATIBLE WITH TTL
- 8-BIT DATA REGISTER INCLUDED
- AVAILABLE IN SMALLER CAPACITIES
- STANDARD 2048 WORDS, 8 BITS/WORD
- PROGRAM EASILY ALTERED
- 14 DAY DELIVERY



ROM BOARD READY FOR CUSTOM BRAIDING

**DESCRIPTION:** The UI-1003 is a low cost, high speed transformer type Read-Only Memory designed with reliability and program alterability in mind. It includes address decoding, core matrix, and output data registers. It is TTL (74 series) compatible and uses a 5 volt supply. Incorporating state-of-the-art MSI circuits, it has a 350 NS read cycle time. Use of toroidal cores insures an uninterrupted magnetic path resulting in a rugged, reliable memory.

There is also available a low cost 256 x 8 ROM identical to the UI-1003 in every detail except size, it is 7-5/8 in. x 4 in. x 3/4 in. It is ideal in code conversion and character generation applications. Ease of memory changeability and 14 day delivery make it attractive as a breadboard or pre-MOS memory. Other word size memories are available and a special department is ready to discuss custom memory requirements.

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## SYSTEM SPECIFICATIONS

Cycle Time ..... 350 nanoseconds  
Access Time ..... 175 nanoseconds  
Capacity ..... Variable word and bit length up to  
2048 words, 8 bits/word

### INPUT SIGNALS — TTL74 Series Compatible

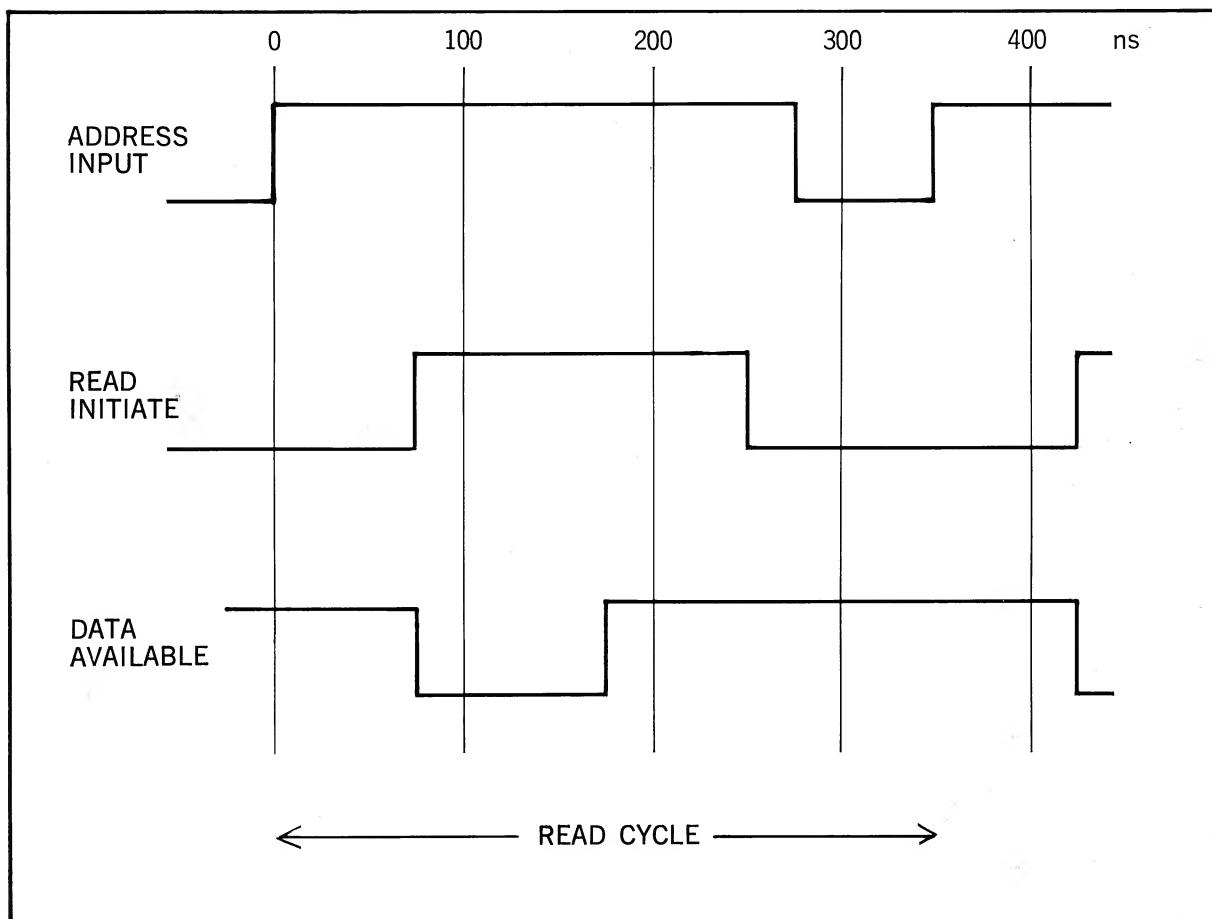
Logic "0" Maximum ..... 0.4V  
Logic "1" Minimum ..... 2.4V  
Address ..... Up to 11 lines, Single Rail  
Cycle Initiate ..... 1 line

### OUTPUT SIGNALS

Logic "0" Maximum ..... 0.4V at 16 ma.  
Logic "1" Minimum ..... 2.4V  
Data ..... Up to 8 bits, Dual Rail, 16 lines  
true and complement available

Power Supply Requirements ..... 5V  $\pm$  5% at 500 ma.  
Operating Temperature Range ..... 0°C to 55°C  
Mechanical Dimensions ..... 7 $\frac{1}{8}$  in. x 7 $\frac{1}{2}$  in. x 3 $\frac{3}{4}$  in.  
Plugs into 86 pin connector (156 mil centers)

## READ/ONLY MEMORY TIMING DIAGRAM



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